

Lecture 2

Linear Circuit Analysis and Dynamical Characteristics

References

Webster, Ch. 1 (Sec. 1.9, 1.10).

The Design and Analysis of Linear Circuits, 6th Ed., Thomas, Rosa and Toussaints,
Wiley 2009 (MAE 140 textbook).

http://en.wikipedia.org/wiki/Thévenin%27s_theorem

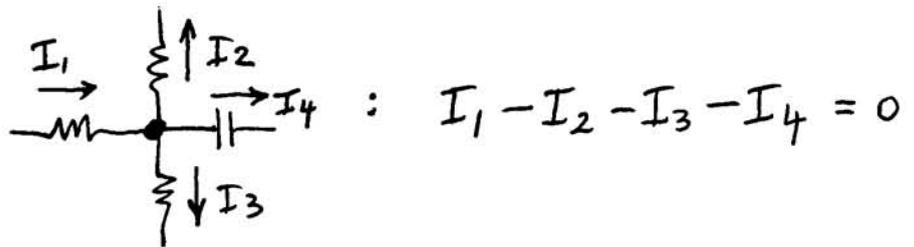
Review of linear circuits

See, e.g.: *The Analysis and Design of Linear Circuits*, 6th Ed, Thomas, Rosa & Toussaint, Wiley 2009 (MAE 140 book).

- Kirchhoff's current law (KCL): conservation of charge

$$\sum I \text{'s into any node} = 0$$

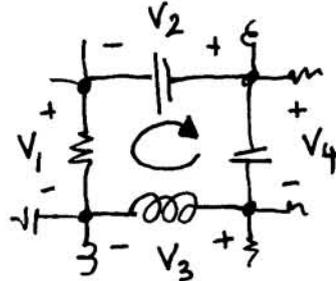
↓
currents, with sign.
Choose the direction of the arrows, and be consistent.



- Kirchhoff's voltage law (KVL): conservation of energy

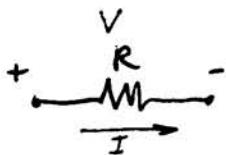
$$\sum V \text{'s around any closed loop} = 0$$

↓
voltages, with sign.
Sometimes in terms of node voltages: V_a V_b
 $- V +$: $V = V_b - V_a$

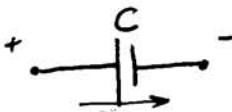


$$V_1 + V_2 - V_4 - V_3 = 0$$

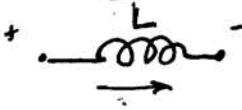
- Circuit elements: relate voltage and current (or their derivatives)
→ impedance Z



$$V = R \cdot I, \text{ or } Z = R$$

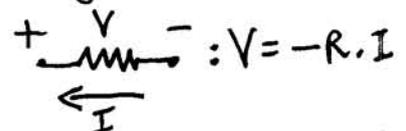


$$I = C \frac{dV}{dt}, \text{ or } Z = \frac{1}{j\omega C}$$

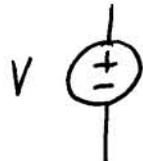


$$V = L \frac{dI}{dt}, \text{ or } Z = j\omega L$$

Note: be consistent with polarities of I and V
e.g.:



— Independent sources : inputs and supplies

V  : DC voltage (battery, power supply, voltage regulator)

$V(t)$  : AC voltage source (or system input)

typically modeled as sinusoidal: $V = A e^{j\phi} e^{j\omega t}$
 amplitude \downarrow phase \downarrow radial frequency \downarrow

NOTE: V_{pp} : peak to peak voltage = $2A$

V_{rms} : root mean square (rms) voltage = $\frac{A}{\sqrt{2}}$

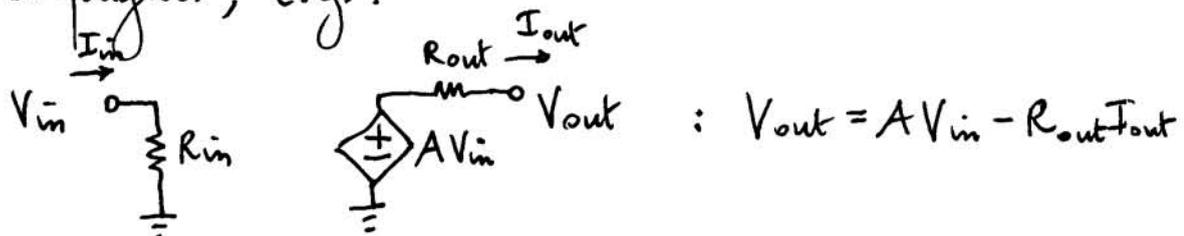
I  : DC current source (bias)

AC current source (system input)

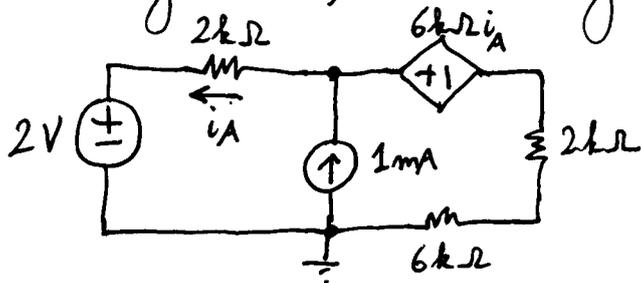
— Dependent sources : voltage or current sources that depend on other voltages or currents in the circuit.

 voltage ;  current

Typically used to represent models of more complex circuits such as amplifiers, e.g.:

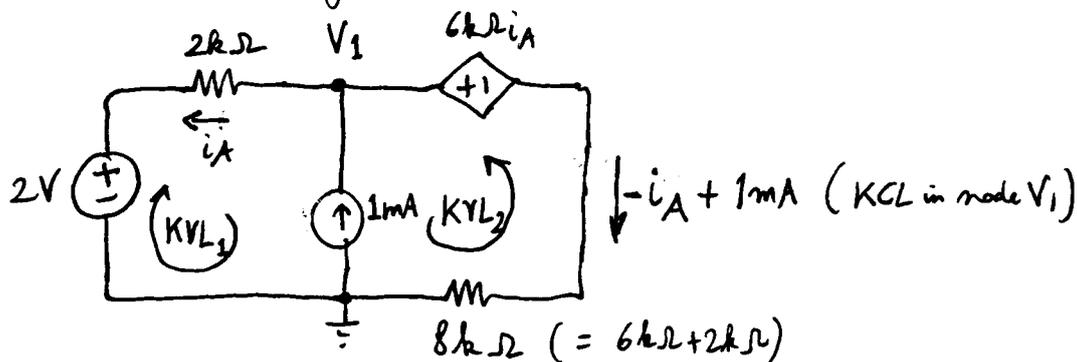


Example: find i_A in the following circuit:



Solution:

Label all unknown voltages & currents, write out known voltages and currents (from KVL & KCL), and combine circuit elements:



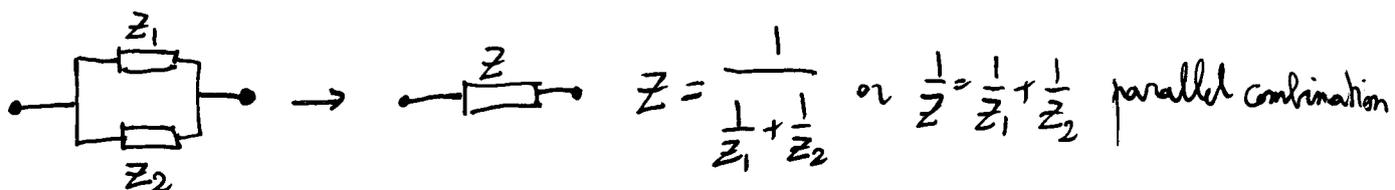
$$\text{KVL}_1: V_1 = 2V + 2k\Omega \cdot i_A$$

$$\text{KVL}_2: V_1 = 8k\Omega (-i_A + 1mA) + 6k\Omega i_A = 8V - 2k\Omega \cdot i_A$$

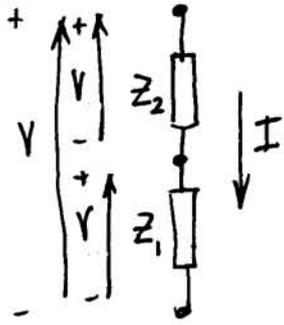
$$\text{Eliminating } V_1: 4k\Omega i_A = 6V$$

$$\Rightarrow i_A = 1.5mA$$

Note: combining circuit elements:



• Series combination:



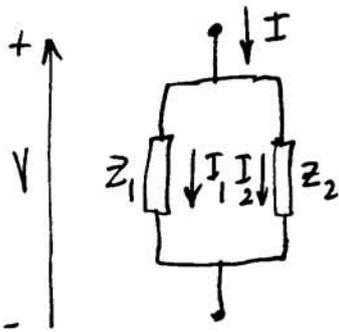
$$\text{KCL: } I_1 = I_2 = I$$

$$\begin{aligned} \text{KVL: } V &= V_1 + V_2 \\ &= Z_1 \cdot I + Z_2 \cdot I \end{aligned}$$

$$\Rightarrow Z = \frac{V}{I} = \frac{Z_1 \cdot I + Z_2 \cdot I}{I} = Z_1 + Z_2$$

\Rightarrow IMPEDANCES ADD IN SERIES

• Parallel combination:



$$\begin{aligned} \text{KCL: } I &= I_1 + I_2 \\ &= \frac{V_1}{Z_1} + \frac{V_2}{Z_2} \end{aligned}$$

$$\text{KVL: } V_1 = V_2 = V$$

$$\Rightarrow Z = \frac{V}{I} = \frac{V}{\frac{V}{Z_1} + \frac{V}{Z_2}} = \frac{1}{\frac{1}{Z_1} + \frac{1}{Z_2}} = \frac{Z_1 \cdot Z_2}{Z_1 + Z_2}$$

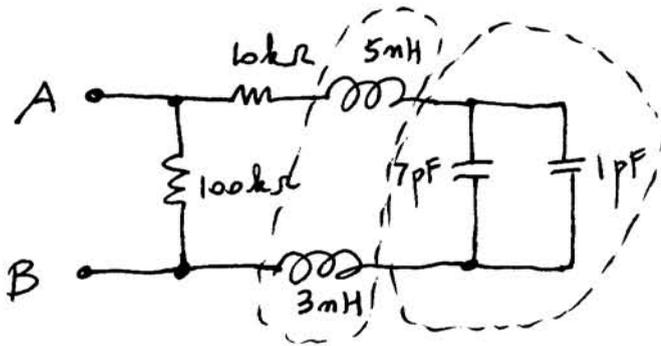
$$\text{or } \frac{1}{Z} = \frac{1}{Z_1} + \frac{1}{Z_2}$$

$$\text{or } Y = Y_1 + Y_2 \quad \begin{array}{l} \text{ADMITTANCE} \\ \text{reciprocal of impedance} \end{array}$$

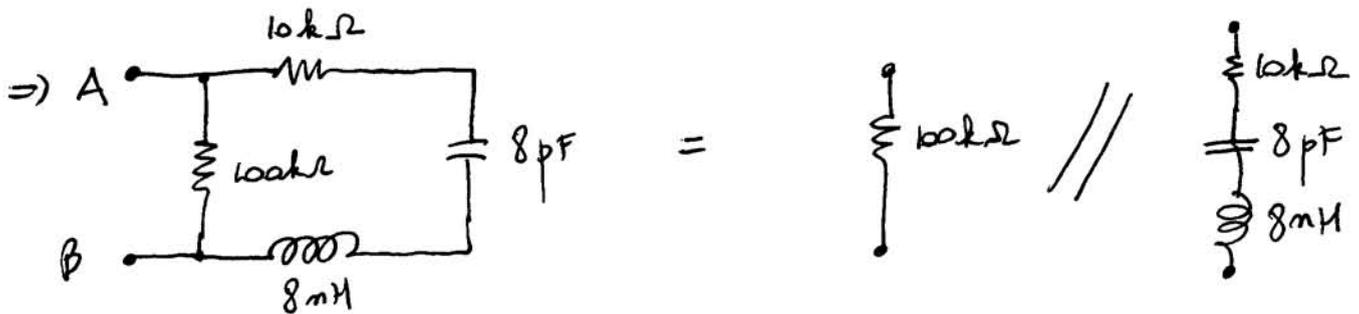
\Rightarrow ADMITTANCES ADD IN PARALLEL

• Series and parallel combinations can be recursively combined to find impedance of more complex circuits:

Example: find the impedance of the following circuit between nodes A and B:



- R : $Z = R \Rightarrow$ resistances add in SERIES
 L : $Z = j\omega L \Rightarrow$ inductances add in SERIES
 C : $Z = \frac{1}{j\omega C} \Rightarrow$ capacitances add in PARALLEL



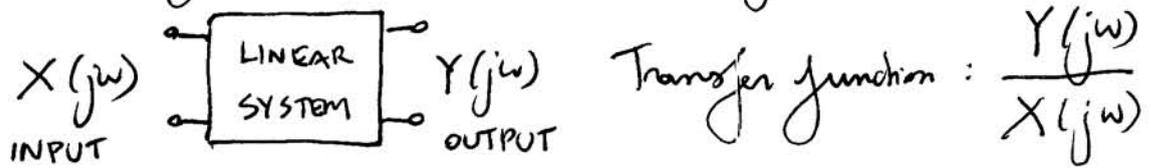
$$\Rightarrow Z = \frac{100k\Omega \cdot \left(j\omega 8mH + 10k\Omega + \frac{1}{j\omega 8pF} \right)}{100k\Omega + j\omega 8mH + 10k\Omega + \frac{1}{j\omega 8pF}} \quad \begin{matrix} (\times j\omega 8pF) \\ (\times j\omega 8pF) \end{matrix}$$

$$= 100k\Omega \cdot \frac{1 + j\omega 80ms - \omega^2 64 \cdot 10^{-21}s^2}{1 + j\omega 880ms - \omega^2 64 \cdot 10^{-21}s^2}$$

Sanity check: $Z = 100k\Omega$ (leftmost branch only)
 for $\omega \rightarrow 0$ and for $\omega \rightarrow \infty$

Indeed: $\frac{1}{j\omega C}$ are open circuit for $\omega \rightarrow 0$
 $j\omega L$ are open circuit for $\omega \rightarrow \infty$

Static and dynamical circuits and systems



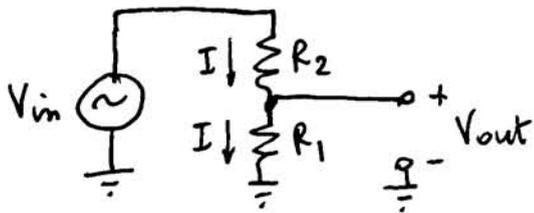
- X and Y can be voltage or current, or other signal types
- $j\omega$ is sometimes written as $D = \frac{d}{dt}$, differential operator for use in the time domain.

ZERO-ORDER INSTRUMENT (static system):

linear system with constant transfer function: static gain (or sensitivity) independent of frequency.

Circuits with only resistors are always static: the output follows the input instantaneously.

Example: Attenuator implemented as VOLTAGE DIVIDER:



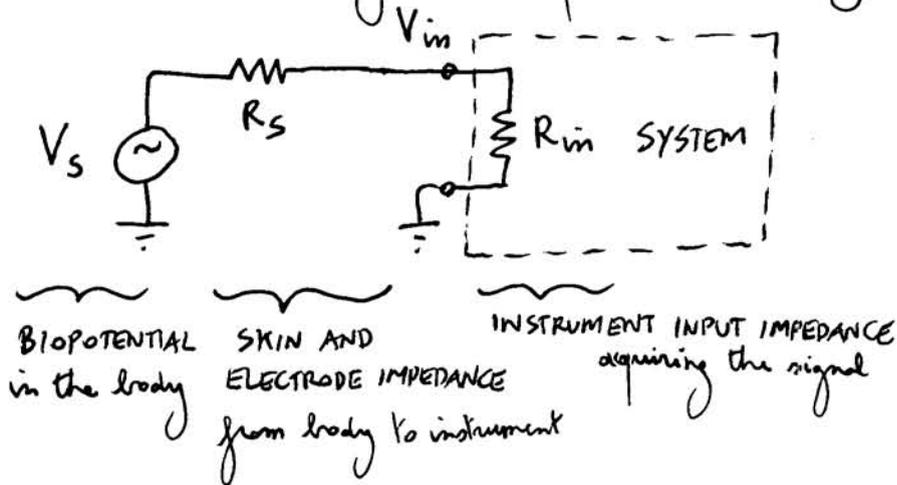
If the output is not loaded, then the same current I flows through R_1 and R_2 .

$$\left. \begin{array}{l} V_{in} = R_1 I + R_2 I \quad (\text{KVL}) \\ V_{out} = R_1 \cdot I \end{array} \right\} \Rightarrow V_{out} = \frac{R_1}{R_1 + R_2} \cdot V_{in}$$

Attenuation: $\frac{V_{out}(j\omega)}{V_{in}(j\omega)} = A_v = \frac{R_1}{R_1 + R_2}$ where $0 \leq A_v \leq 1$
independent of frequency

Useful where it is desirable to attenuate the amplitude (or range) of a voltage signal for further processing.

NOTE 1: Attenuation due to voltage division is not always intentional in the design, and may be the effect of non-ideal source and system impedances, e.g.:

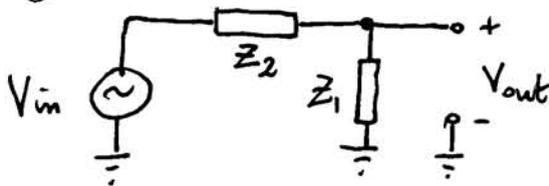


$$\Rightarrow V_{in} = \frac{R_{in}}{R_{in} + R_s} \cdot V_s \quad \Rightarrow \text{GOOD DESIGN: } V_{in} \approx V_s$$

\downarrow \downarrow \downarrow
 signal as measured by the instrument real signal in the body for $R_{in} \gg R_s$

- \rightarrow minimize skin and electrode impedance
- \rightarrow maximize instrument input impedance

NOTE 2: For frequency-dependent impedances rather than resistors, voltage division may implement frequency-dependent transfer functions:

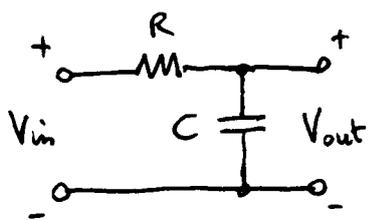


$$\frac{V_{out}(j\omega)}{V_{in}(j\omega)} = \frac{Z_1(j\omega)}{Z_1(j\omega) + Z_2(j\omega)}$$

Dynamical systems can be readily designed by choosing and implementing $Z_1(j\omega)$ and $Z_2(j\omega)$:

- FIRST-ORDER INSTRUMENT: $\frac{Y(j\omega)}{X(j\omega)} = \frac{b_1 j\omega + b_0}{d_1 j\omega + d_0}$ (single pole)

Example: RC first-order lowpass filter: $\frac{V_{out}(j\omega)}{V_{in}(j\omega)} = \frac{1}{1 + j\omega Z}$



$$\left. \begin{aligned} Z_1(j\omega) &= \frac{1}{j\omega C} \\ Z_2(j\omega) &= R \end{aligned} \right\} \Rightarrow$$

$$\frac{V_{out}(j\omega)}{V_{in}(j\omega)} = \frac{\frac{1}{j\omega C}}{\frac{1}{j\omega C} + R} = \frac{1}{1 + j\omega RC}$$

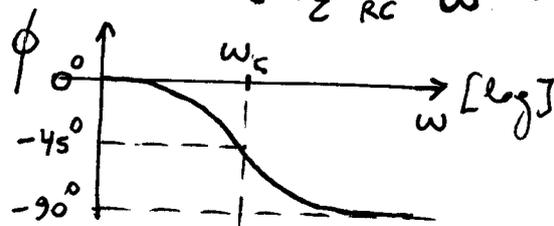
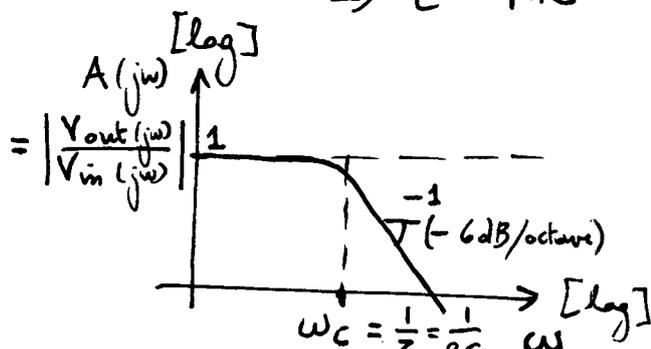
$$\Rightarrow Z = R \cdot C$$

• Frequency response:

$$\frac{V_{out}(j\omega)}{V_{in}(j\omega)} = A(j\omega) \angle \phi(j\omega)$$

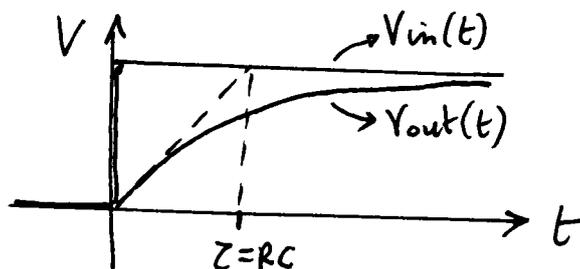
$$A(j\omega) = \frac{1}{\sqrt{1 + \omega^2 Z^2}}$$

$$\phi(j\omega) = -\arctan(\omega Z)$$



• Step response:

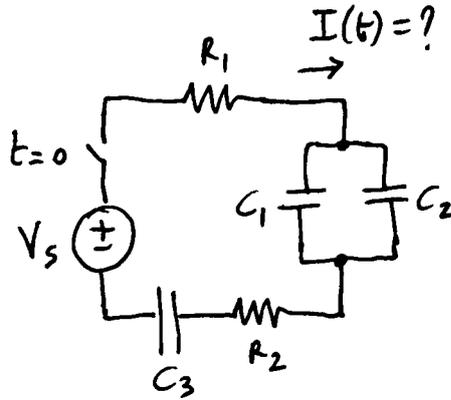
$$V_{out}(t) = 1 - e^{-t/Z}$$



More generally: linear circuits with resistive elements (R's) and with one equivalent dynamical element (L or C), have first order dynamics.

NOTE: combine series or parallel instances of same type (L or C) dynamical elements into single equivalent elements.

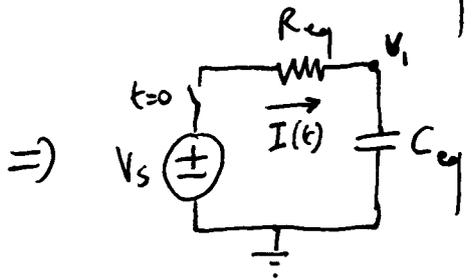
Example:



$$C_1 \parallel C_2 \rightarrow C_1 + C_2 \quad (\text{parallel capacitances add})$$

$$\begin{matrix} C_1 + C_2 \\ C_3 \end{matrix} \rightarrow C_{eq} = \frac{(C_1 + C_2)C_3}{C_1 + C_2 + C_3} \quad (\text{series capacitances combine reciprocally, additive in } \frac{1}{C})$$

$$R_1 \text{ --- } R_2 \rightarrow R_{eq} = R_1 + R_2$$



$$\left\{ \begin{array}{l} C_{eq} \frac{dV_1}{dt} = I(t) \text{ and} \\ V_s = V_1 + R_{eq} \cdot I(t) \quad (\text{for } t \geq 0) \end{array} \right.$$

2 equations; eliminate V_1 by differentiating the second:

$$0 = \frac{dV_1}{dt} + R_{eq} \cdot \frac{dI}{dt}$$

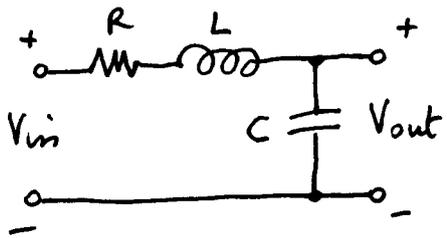
$$\Rightarrow - \underbrace{R_{eq} \cdot C_{eq}}_{= \tau} \cdot \frac{dI}{dt} = I \quad \Rightarrow \quad I = I_0 \cdot e^{-t/\tau}$$

where I_0 is given by initial conditions.

e.g. if $V_1(0) = 0$ (no initial charge on the capacitor)
then $I_0 = V_s / R_{eq}$

- SECOND-ORDER INSTRUMENT: $\frac{Y(j\omega)}{X(j\omega)} = \frac{b_2(-\omega^2) + b_1 j\omega + b_0}{a_2(-\omega^2) + a_1 j\omega + a_0}$
 (two poles, possibly complex)

Example: RLC second-order lowpass: $\frac{V_{out}(j\omega)}{V_{in}(j\omega)} = \frac{1}{1 + \frac{2\zeta j\omega}{\omega_m} - \frac{\omega^2}{\omega_m^2}}$



$Z_1(j\omega) = \frac{1}{j\omega C}$

$Z_2(j\omega) = R + j\omega L$

ω_m : NATURAL FREQUENCY
 ζ : DAMPING RATIO

$\Rightarrow \frac{V_{out}(j\omega)}{V_{in}(j\omega)} = \frac{\frac{1}{j\omega C}}{\frac{1}{j\omega C} + R + j\omega L} = \frac{1}{1 + j\omega RC - \omega^2 LC} \Rightarrow \begin{cases} \omega_m = \frac{1}{\sqrt{LC}} \\ \zeta = \frac{RC \cdot \omega_m}{2} \end{cases}$

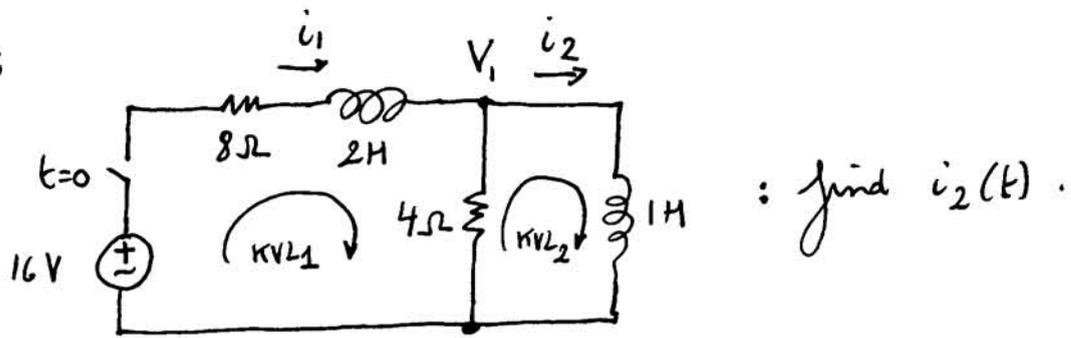
Frequency response and step response: see book for details...

In essence:

- $\zeta < 1 \Rightarrow$ UNDERDAMPED (two complex poles):
 frequency resonance: peak near $\omega = \omega_m$;
 oscillatory step response
- $\zeta > 1 \Rightarrow$ OVERDAMPED (two real distinct poles):
 no resonance; slow, oscillation-free step response
- $\zeta = 1 \Rightarrow$ CRITICALLY DAMPED (two identical real poles):
 "sweet spot": fastest without resonance/oscillations

More generally: linear circuits with resistive elements (R's) and with TWO equivalent dynamical elements (L or C), have SECOND-ORDER dynamics.

Example:



: find $i_2(t)$.

Solution: use KVL, KCL to get a second-order ODE.

(no sinusoidal input, so use $D = \frac{d}{dt}$ rather than $j\omega$!)

$$\text{KVL}_1: -16 + 8i_1 + 2 \frac{di_1}{dt} + 4(i_1 - i_2) = 0 \quad (1)$$

KCL @ V_1 node

$$\text{KVL}_2: 4(i_2 - i_1) + 1 \cdot \frac{di_2}{dt} = 0 \quad (2)$$

Eliminate i_1 :

$$(2): i_1 = i_2 + \frac{1}{4} \frac{di_2}{dt} \quad \left. \begin{array}{l} \\ \frac{d}{dt}(2): \frac{di_1}{dt} = \frac{di_2}{dt} + \frac{1}{4} \frac{d^2i_2}{dt^2} \end{array} \right\} \text{substitute in (1)}$$

$$\Rightarrow -16 + (8i_2 + 2 \frac{di_2}{dt}) + (2 \frac{di_2}{dt} + \frac{1}{2} \frac{d^2i_2}{dt^2}) + \frac{di_2}{dt} = 0$$

$$\text{or } \frac{d^2i_2}{dt^2} + 10 \frac{di_2}{dt} + 16i_2 = 32$$

Linear ODE lang of tricks: try a solution of the form:

$$i_2(t) = \underbrace{i_{2\infty}}_{\text{STEADY STATE SOLUTION}} + \underbrace{A_1 e^{\lambda_1 t} + A_2 e^{\lambda_2 t}}_{\text{TRANSIENT; HOMOGENEOUS SOLUTION}}$$

- STEADY STATE: $\frac{d}{dt} = 0$ and $\frac{d^2}{dt^2} = 0 \Rightarrow 16i_{2\infty} = 32$ or $i_{2\infty} = 2$

- HOMOGENEOUS SOL.: $\lambda^2 + 10\lambda + 16 = 0 \Rightarrow$ roots: $\left. \begin{array}{l} \lambda_1 = -2 \\ \lambda_2 = -8 \end{array} \right\}$
characteristic equation

and A_1, A_2 are given by initial conditions of i_2 and $\frac{di_2}{dt}$.

- Steady state:

Often we are not interested in transients, but in the output or behavior of the circuit when it settles into STEADY STATE.

- For sinusoidal inputs: use $j\omega$ rather than $D = \frac{d}{dt}$, no need to solve ODEs!

→ Find transfer function $\frac{Y(j\omega)}{X(j\omega)} = A(\omega) e^{j\phi(\omega)}$

\downarrow GAIN (amplification) \downarrow PHASE

$$X(t) = \cos(\omega t) \Rightarrow Y(t) = A(\omega) \cdot \cos(\omega t + \phi(\omega))$$

in steady state

- For constant inputs (such as voltage/current supplies): same, in the limit $\omega \rightarrow 0$

$$\frac{1}{\text{C}} : Z = \frac{1}{j\omega C} \rightarrow \infty \Rightarrow \text{OPEN CIRCUIT}$$

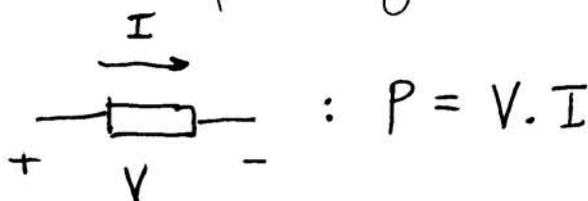
→ remove all capacitors!

$$\text{L} : Z = j\omega L \rightarrow 0 \Rightarrow \text{SHORT CIRCUIT}$$

→ all inductors become wires!

- Power dissipation: critical for bioinstrumentation, especially implanted and wireless systems!

- Power dissipated by circuit elements:

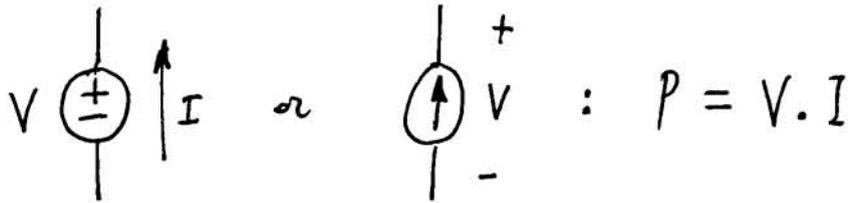


$$: P = V \cdot I$$

$$\rightarrow R: P = RI^2 = \frac{V^2}{R}$$

↘ L and C: I and V 90° out of phase ⇒ ZERO AVERAGE POWER

- Power supplied by independent sources:



Note the direction of current for positive power delivered: from lower to higher potential

- Total power consumed by a circuit:

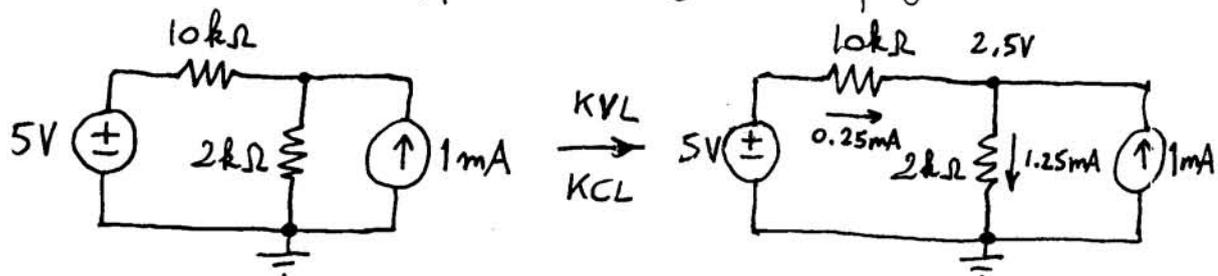
METHOD 1: Sum the power dissipated by all resistors in the circuit.

→ does not account for power consumed by dependent sources, e.g. amplifiers in the circuit

METHOD 2: Sum the power supplied by all independent sources in the circuit, e.g. the power supplies.

→ do not include dependent sources (e.g. amplifiers) in the sum, but include their power supplies (e.g., supply drain of an amplifier)

Example:



METHOD 1: $P_{\text{total}} = P_{10k\Omega} + P_{2k\Omega} = 2.5V \times 0.25mA + 2.5V \times 1.25mA = 3.75mW$

METHOD 2: $P_{\text{total}} = P_{5V} + P_{1mA} = 5V \times 0.25mA + 2.5V \times 1mA = 3.75mW \text{ OK!}$

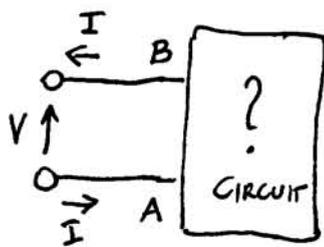
- AC power, e.g. power drawn from a biosignal source:

Average power @ ω : $P = \langle V(t) \cdot I(t) \rangle = \frac{1}{2} \text{Real} (V(j\omega) \cdot I^*(j\omega))$
(steady state)

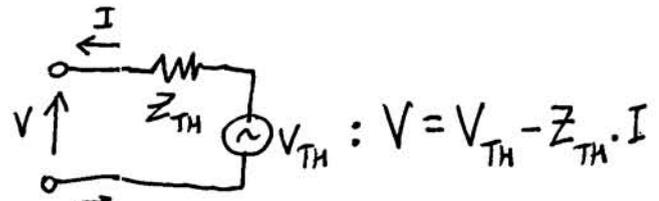
Thévenin and Norton equivalents:

→ useful to find input and output impedance, and transfer function, of a linear system such as an amplifier, filter, etc...

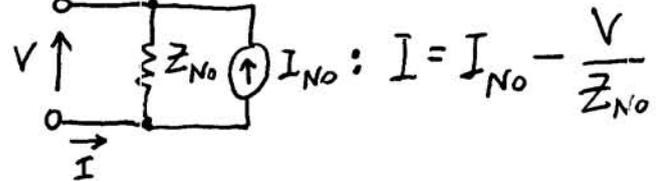
→ reduces the circuit, as observed from a particular port, to a simpler equivalent circuit with just a single source and a single impedance element.



THÉVENIN EQUIVALENT:



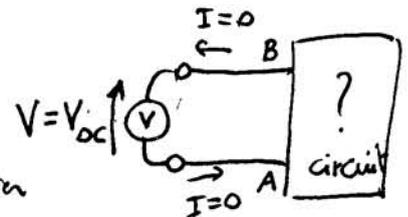
NORTON EQUIVALENT:



→ the source and impedance equivalents are found by open-circuit voltage and closed-circuit current measurements (or calculations) over the port:

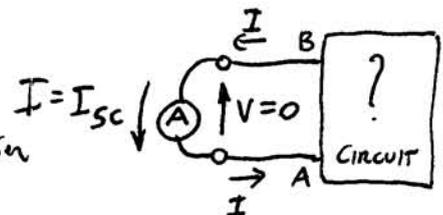
- $V_{TH} = V_{OC}$

OPEN-CIRCUIT VOLTAGE ;
as measured with ideal voltmeter



- $I_{NO} = I_{SC}$

SHORT-CIRCUIT CURRENT ;
as measured with ideal ammeter



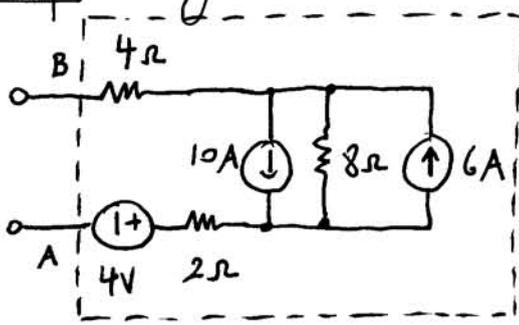
- $Z_{TH} = Z_{NO} = \frac{V_{OC}}{I_{SC}}$

IMPEDANCE :

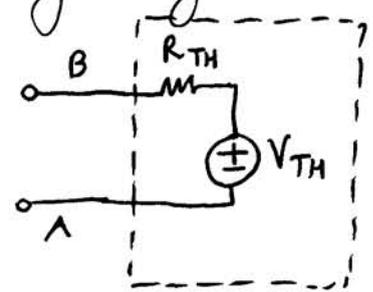
also found as the impedance of the circuit between nodes A and B, by "killing" all internal independent sources :

\oplus and \ominus → SHORT CIRCUIT (0V) ; \downarrow → OPEN CIRCUIT (0A)

Example: find the Thévenin equivalent of the following circuit:

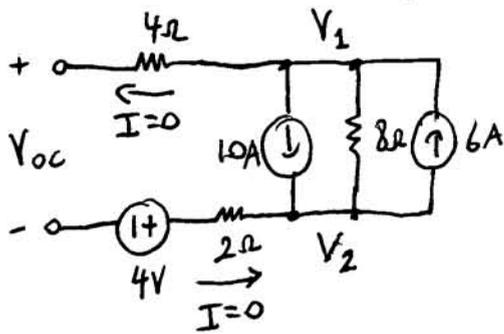


→
from the
outside, the
same



$R_{TH} = ? \quad V_{TH} = ?$

- $V_{TH} = V_{OC}$: open circuit:



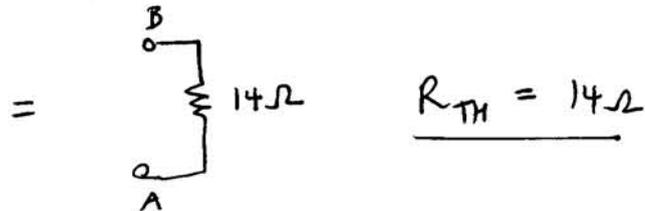
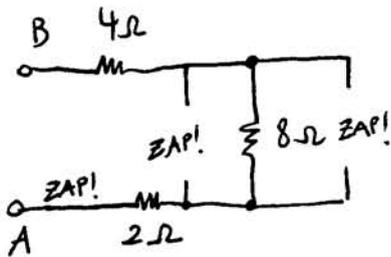
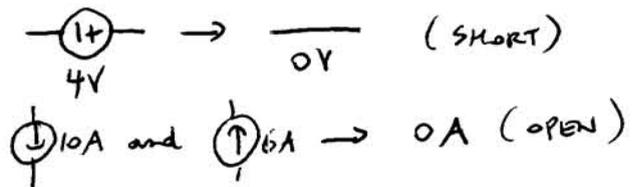
• $I=0 \Rightarrow$ no voltage drop across 4Ω and 2Ω resistors

\Rightarrow KVL: $V_{OC} = V_1 - V_2 + 4V$

• KCL in V_1 (or V_2): $10A + \frac{V_1 - V_2}{8\Omega} + (-6A) = 0$

$\Rightarrow V_1 - V_2 = -32V \Rightarrow \underline{V_{TH} = V_{OC} = -28V}$

- R_{TH} : "kill" the sources :



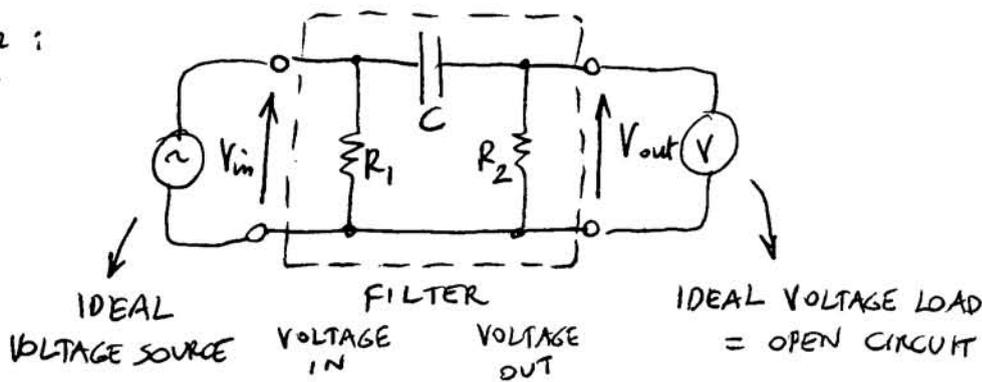
NOTE : only independent sources should be "killed"; dependent sources remain, as they change with current or voltage.

Thévenin/Norton equivalents at input and output ports of a system give the input and output impedance and transfer function of the system.

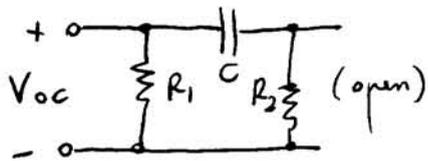
→ The equivalent at the input, for an ideal load at the output, gives the input impedance.

→ The equivalent at the output, for an ideal source at the input, gives the transfer function and output impedance.

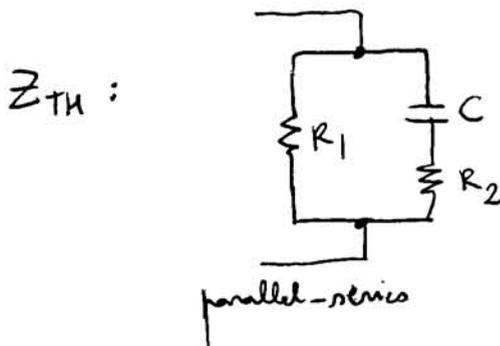
Example:



a. INPUT Voltage signal \Rightarrow THÉVENIN (Current \Rightarrow NORTON)



No sources $\Rightarrow V_{oc} = 0$ (OK!)

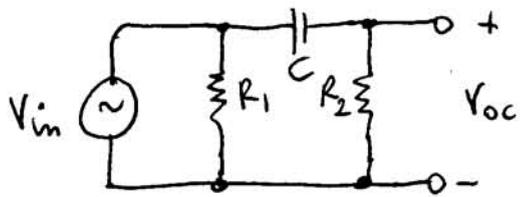


$$Z_{TH} = \frac{R_1 (R_2 + \frac{1}{j\omega C})}{R_1 + R_2 + \frac{1}{j\omega C}} = R_1 \cdot \frac{1 + j\omega R_2 C}{1 + j\omega (R_1 + R_2) C}$$

$$\Rightarrow Z_{in} = R_1 \cdot \frac{1 + j\omega R_2 C}{1 + j\omega (R_1 + R_2) C}$$

b. OUTPUT

Voltage signal \Rightarrow THÉVENIN



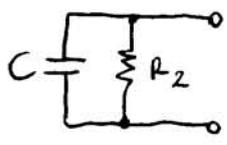
R_1 doesn't matter as it is driven by V_{in} !

Voltage divider:
$$V_{oc} = \frac{R_2}{R_2 + \frac{1}{j\omega C}} \cdot V_{in} = \frac{j\omega R_2 C}{1 + j\omega R_2 C} \cdot V_{in}$$

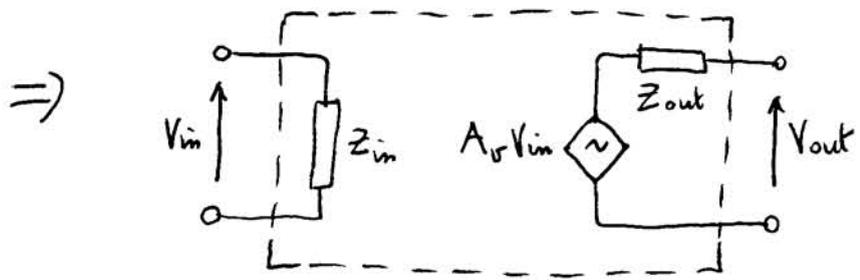
$\Rightarrow V_{TH} = A_V(j\omega) \cdot V_{in}$, where $A_V(j\omega) = \frac{j\omega R_2 C}{1 + j\omega R_2 C}$

is the TRANSFER FUNCTION $\frac{V_{out}(j\omega)}{V_{in}(j\omega)}$

Z_{TH} : \rightarrow SHORT



$$Z_{TH} = \frac{\frac{1}{j\omega C} \cdot R_2}{\frac{1}{j\omega C} + R_2} = \frac{R_2}{1 + j\omega R_2 C} = Z_{out}$$



$$Z_{in}(j\omega) = R_1 \cdot \frac{1 + j\omega R_2 C}{1 + j\omega(R_1 + R_2)C} \quad A_V(j\omega) = \frac{j\omega R_2 C}{1 + j\omega R_2 C} \quad Z_{out}(j\omega) = \frac{R_2}{1 + j\omega R_2 C}$$

Exercise: How do transfer function and output impedance change if input is CURRENT rather than VOLTAGE?