

520.492 Mixed-Signal VLSI Systems

Week 7

Linear Filters

References

1. Geiger, Allen and Strader, Sections 8.4 and 8.5 (pp 673-728).
2. C.A. Mead, *Analog VLSI and Neural Systems* (Addison Wesley, 1992), pp 127-192.
3. Franca and Tsividis, Chapters 6 and 7.

LINEAR FILTERS

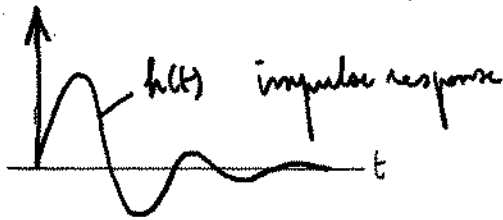
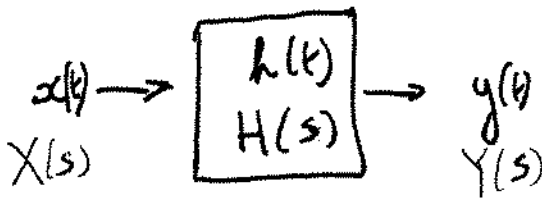
Geiger et al.: chapters 8.4 & 8.5 (pp 673-728)
 Carver Mead, Analog VLSI and Neural Syst (pp 127-192)
 Franca & Tsividis, Chapter 6 & 7

CONTINUOUS-TIME FILTERS

→ Laplace: $s = \frac{d}{dt}$
 → Fourier: $j\omega = \frac{d}{dt}$

$$F(s) = \int_0^{\infty} f(t) e^{-st} dt$$

$$F(\omega) = \int_{-\infty}^{\infty} f(t) e^{-j\omega t} dt$$



$$y(t) = \int_0^t h(t-\theta) x(\theta) d\theta$$

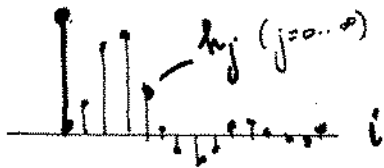
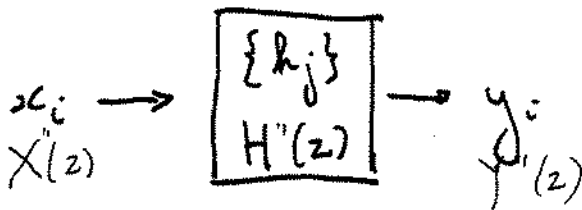
$$Y(s) = H(s) \cdot X(s)$$

→ CONTINUOUS-TIME ANALOG

DISCRETE-TIME (SAMPLED) FILTERS

→ z-transform: $z = [t \rightarrow t + \frac{1}{f_s}]$

$$F''(z) = \sum_{i=0}^{+\infty} f_i \cdot z^{-i}$$



$$y_i = \sum_{j=-\infty}^0 h_{i-j} \cdot x_j$$

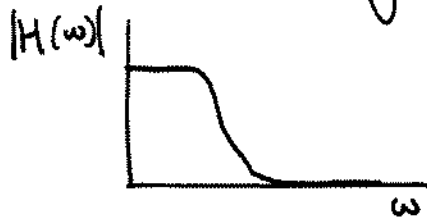
$$Y''(z) = H''(z) \cdot X''(z)$$

finite impulse response (FIR): $h_i \equiv 0$ for $i > i_{max}$
 infinite impulse response (IIR): $h_i \equiv 0$ only for $i < 0$

→ SWITCHED-CAPACITOR FILTERS
 → DIGITAL FILTERS

FILTER CATEGORIES (in frequency domain)

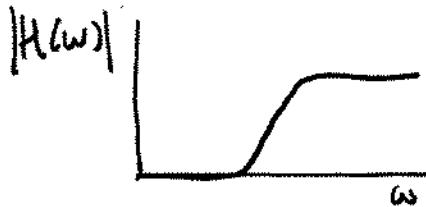
LOWPASS:



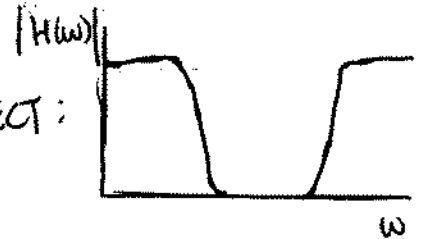
BANDPASS:



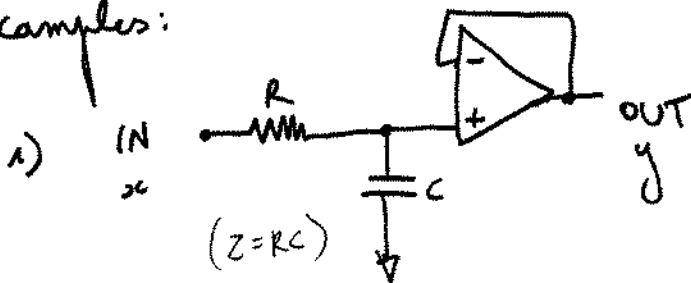
HIGHPASS:



BANDREJECT:

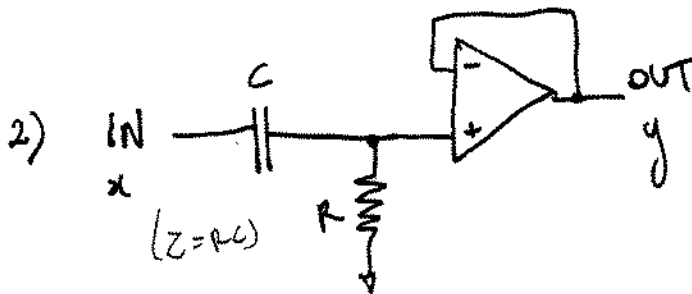
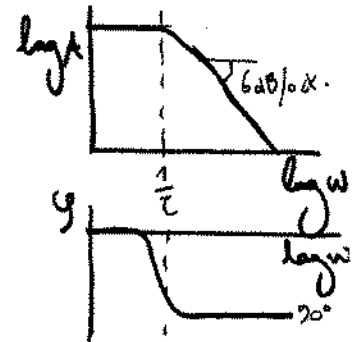


examples:



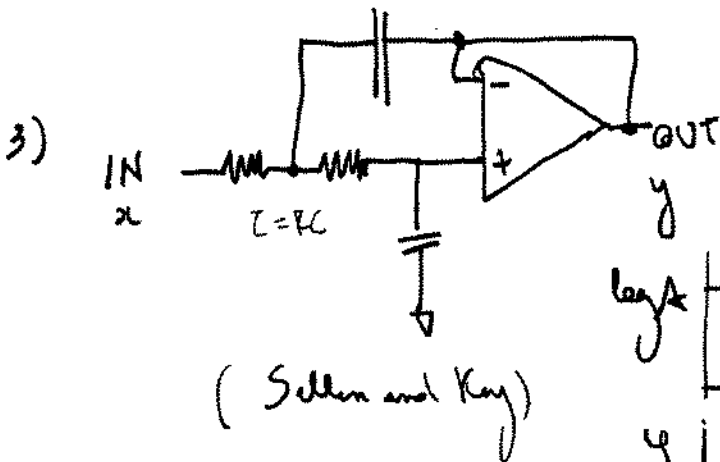
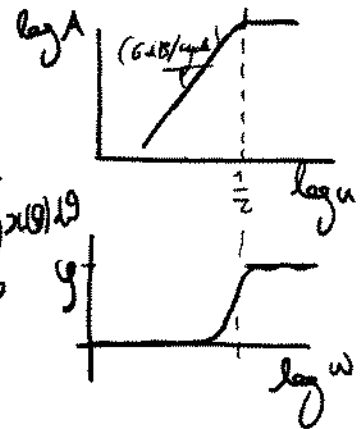
$$Y(s) = \frac{1}{1+Zs}$$

$$y(t) \approx \frac{1}{Z} \int_{-\infty}^t x(\theta) d\theta$$

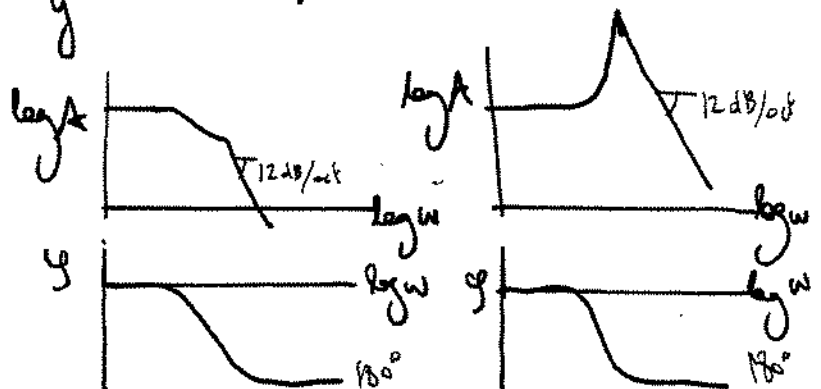


$$Y(s) = \frac{Zs}{1+Zs}$$

$$y(t) \approx x(t) - \frac{1}{Z} \int_{-\infty}^t x(\theta) d\theta$$



$$Y(s) = \frac{1}{1+aZs+b(Zs)^2}$$



FILTER SYNTHESIS

Continuous-time: $x(t) \rightarrow H(s) \rightarrow y(t)$

$$H(s) = \frac{\sum_{j=0}^N b_j s^j}{\sum_{i=0}^M a_i s^i}$$

- Canonical decomposition:

$$s \cdot \vec{z} = \vec{A} \cdot \vec{z} + \vec{B} \cdot x$$

$$y = \vec{C} \cdot \vec{z}$$

→ integrators

- cascade decomposition:

$$H(s) = A \cdot \underbrace{\frac{1 + b_1' s + b_2' s^2}{1 + a_1' s + a_2' s^2}}_{\text{biquad}} \cdot \frac{1 + \dots}{1 + \dots}$$

- Tsividis (2004)

discrete-time: $x^i \rightarrow H(z) \rightarrow y^i$

$$H(z) = \frac{\sum_{j=0}^N b_j' z^j}{1 - \sum_{i=1}^M a_i' z^{-i}}$$

$$z \cdot \vec{z} = \vec{A}' \cdot \vec{z} + \vec{B}' \cdot x$$

$$y = \vec{C}' \cdot \vec{z}$$

→ accumulators

$$H'(z) = A' \cdot \underbrace{\frac{1 + b_1' z^{-1} + b_2' z^{-2}}{1 - a_1' z^{-1} - a_2' z^{-2}}}_{\text{biquad}} \cdot \dots$$

discrete time (direct synthesis)

- FIR: $H'(z) = \sum_{j=0}^N b_j' z^{-j}$ ($a_i' = 0$)

$$\rightarrow y^i = \sum_{j=0}^N b_j' x^{i-j}$$

- IIR: $y^i = \sum_{j=1}^M a_j' y^{i-j} + \sum_{j=0}^N b_j' x^{i-j}$

ANALOG

DIGITAL

CONTINUOUS

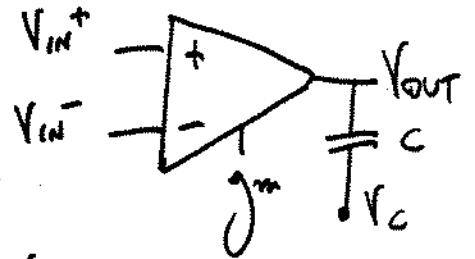
SAMPLED

ANALOG CONTINUOUS-TIME IMPLEMENTATION

• Transconductance mode:

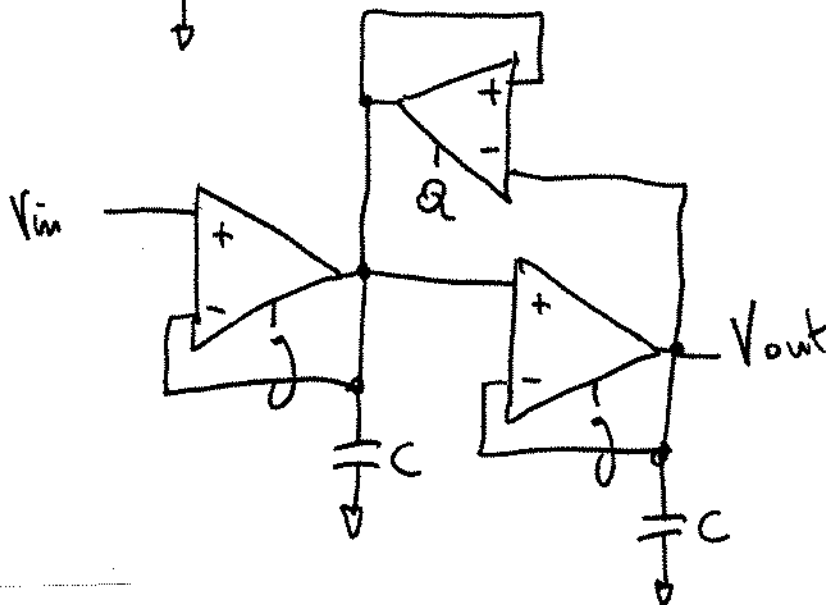
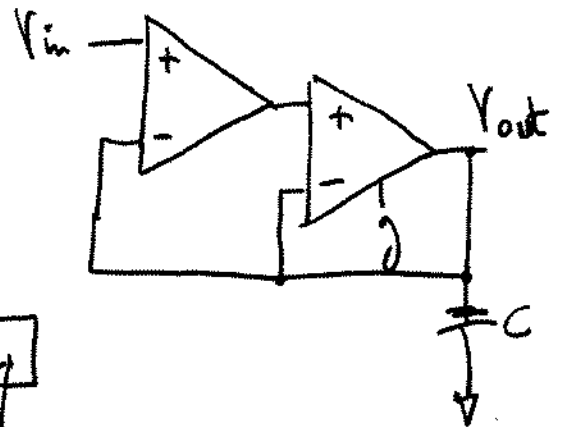
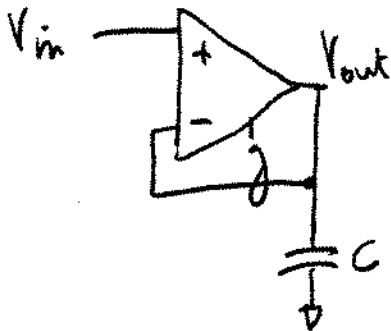
- g_m : voltage in (H-Z) differential
current out (H-Z)
- C : integrating current to voltage conversion

INTEGRATOR :



$$C \frac{dV_{out}}{dt} = C \frac{dV_c}{dt} + (V_{in+} - V_{in-}) g_m$$

examples:

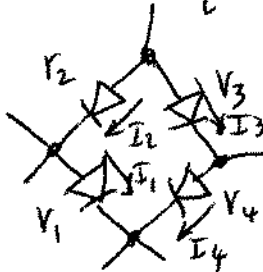


• Current mode : LOG-DOMAIN FILTERS

- Static translinear principle:

$$I \propto e^{V/V_{th}} \text{ then } \sum_i V_i = \sum_j V_j \Rightarrow \prod_i I_i = \prod_j I_j$$

$$(V_{th} = \frac{kT}{q} = 25mV @ 300K)$$

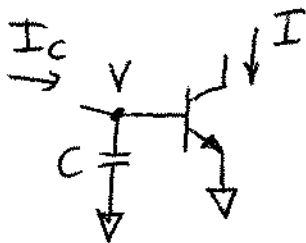


$$V_1 + V_2 = V_3 + V_4 \Rightarrow I_1 I_2 = I_3 I_4$$

also: BJTs, or MOS in subthreshold

- Dynamic translinear principle:

$$I \propto e^{V/V_{th}} \text{ then } \frac{d}{dt} I = \frac{1}{V_{th}} \frac{d}{dt} V \cdot I$$

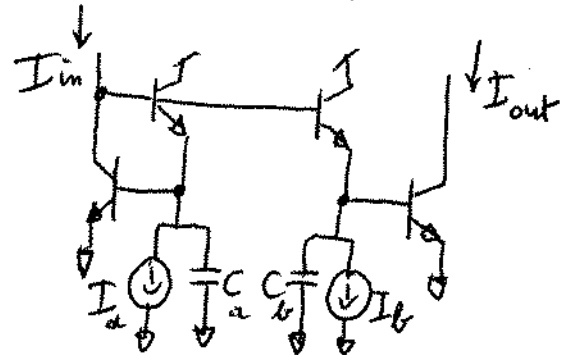
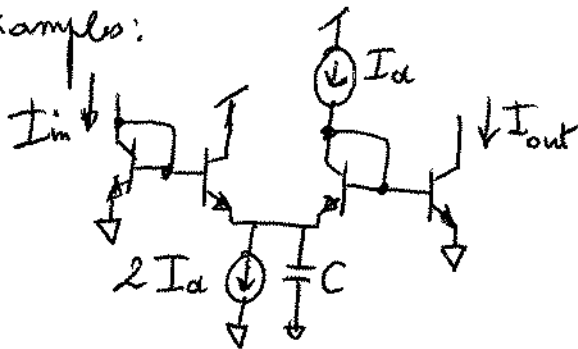


$$I_c = C \frac{dV}{dt} \Rightarrow \frac{dI}{dt} = \frac{I \cdot I_c}{C V_{th}}$$

$$(\beta_F = \infty)$$

Product $I \cdot I_c$ implemented with STATIC translinear principle

Example:



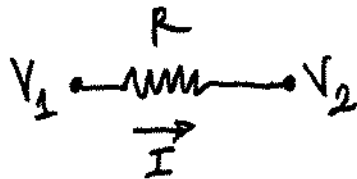
$$\frac{I_{out}}{I_{in}} = \frac{1}{1 + Zs}$$

$$Z = \frac{C V_{th}}{I_a}$$

$$\frac{I_{out}}{I_{in}} = \frac{I_a + C_a V_{th} s}{I_b + C_b V_{th} s}$$

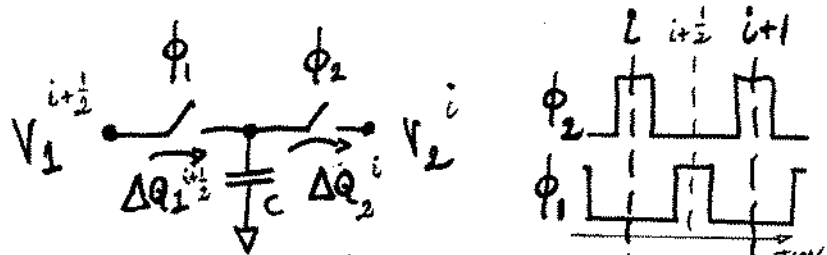
ANALOG DISCRETE-TIME IMPLEMENTATION

Switched capacitors :



$$I(t) = \frac{V_1 - V_2}{R}$$

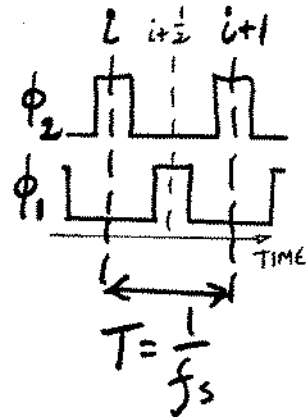
Continuous-time



$$\Delta Q_2^{i+1} = C V_1^{i+1/2} - C V_2^{i+1}$$

$$\Delta Q_1^{i+1/2} = C V_1^{i+1/2} - C V_2^i$$

discrete-time (switched)

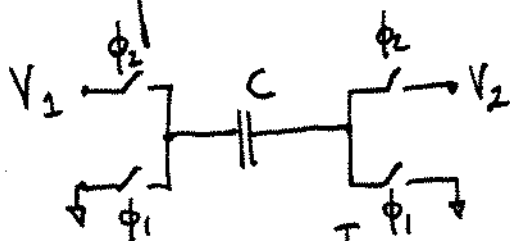


Continuous approximation ($f_N \ll f_s$):

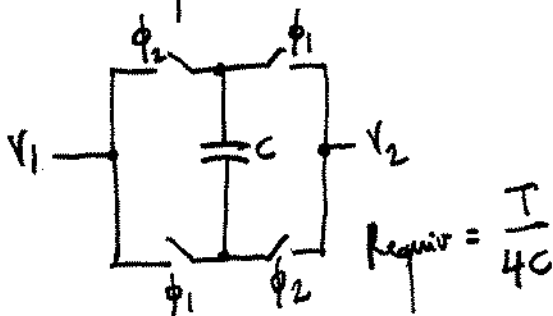
$$V_{1(2)}^{i+1} \approx V_{1(2)}^i \Rightarrow I_{\text{over}} = \frac{\Delta Q}{T} = \frac{C}{T} (V_1 - V_2)$$

$$R_{\text{equiv}} = \frac{T}{C} = \frac{1}{f_s \cdot C}$$

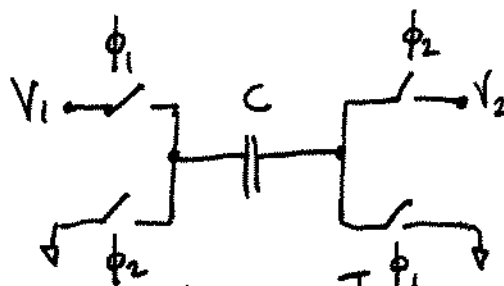
Other implementations :



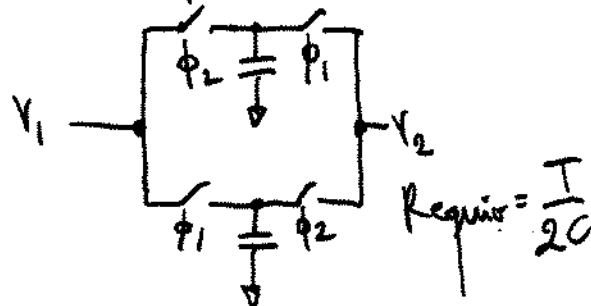
$$R_{\text{equiv}} = \frac{T}{C}$$



$$R_{\text{equiv}} = \frac{T}{4C}$$



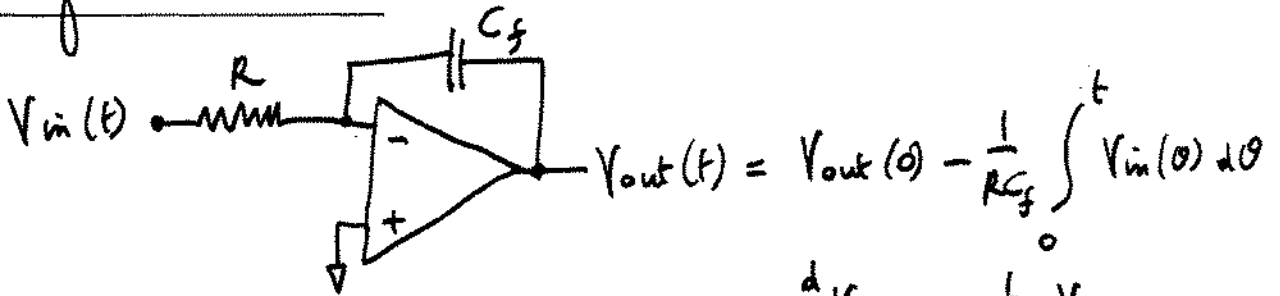
$$R_{\text{equiv}} = -\frac{T}{C}$$



$$R_{\text{equiv}} = \frac{T}{2C}$$

Example: INTEGRATOR ↔ ACCUMULATOR :

integrator (continuous):

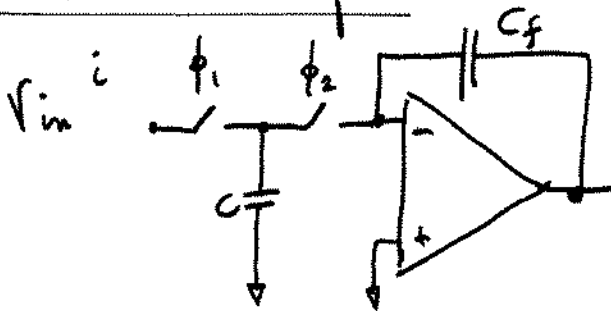


$$V_{out}(t) = V_{out}(0) - \frac{1}{RC_f} \int_0^t V_{in}(0) dt$$

or $\frac{d}{dt} V_{out} = -\frac{1}{RC_f} V_{in}$

$$H(s) = -\frac{1}{RC_f} \cdot \frac{1}{s}$$

accumulator (sampled):

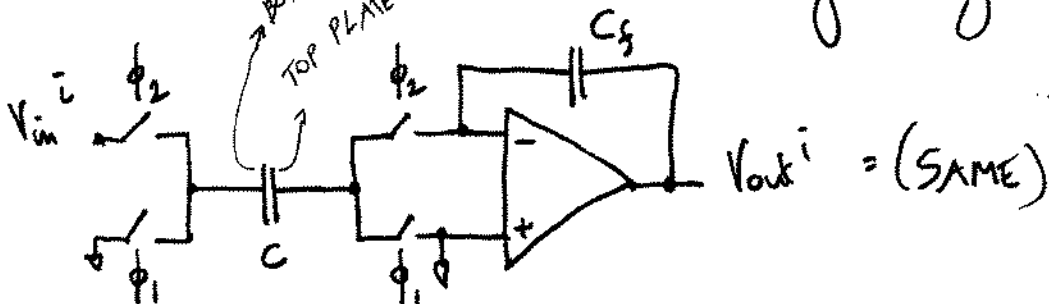


$$V_{out}^i = V_{out}^0 - \frac{C}{C_f} \sum_{j=1}^i V_{in}^j$$

or $V_{out}^i - V_{out}^{i-1} = -\frac{C}{C_f} V_{in}^i$

$$H'(z) = -\frac{C}{C_f} \frac{1}{1-z^{-1}}$$

stray-insensitive version (for better linearity; better clock feedthrough reduction; ...):



$$V_{out}^i = (\text{SAME})$$

Higher frequencies: $f_N \approx f_s$

• Case 1: straight mapping $\frac{1}{R} \leftrightarrow S \cdot \frac{1}{T} \quad C = \frac{T}{R}$

Corresponds to $S \equiv \frac{1-z^{-1}}{T}$ or $\frac{d}{dt} f \equiv \frac{f(t) - f(t-T)}{T}$

$$(z = \frac{1}{1-sT})$$

\Rightarrow distortion of spectrum and impulse response

• Case 2: find mapping $s \leftrightarrow z$ to preserve impulse response

$\Rightarrow z = e^{+sT} \Rightarrow$ synthesize poles zeros of e^{+sT}

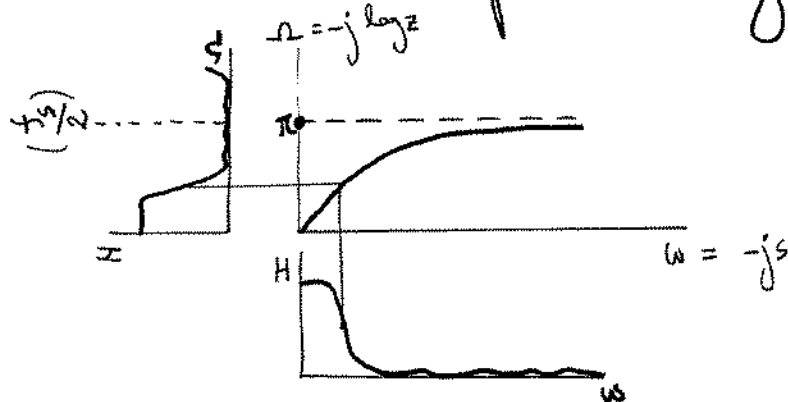
• Case 3: bilinear transformation

$$z = \frac{1 + \frac{sT}{2}}{1 - \frac{sT}{2}}$$

$$\text{or } f(t) - f(t-T) \approx \frac{\frac{df}{dt}(t) + \frac{df}{dt}(t-T)}{2} \cdot T$$

$\approx \int_{t-T}^{t+T} \frac{df}{dt}(\theta) \cdot d\theta$

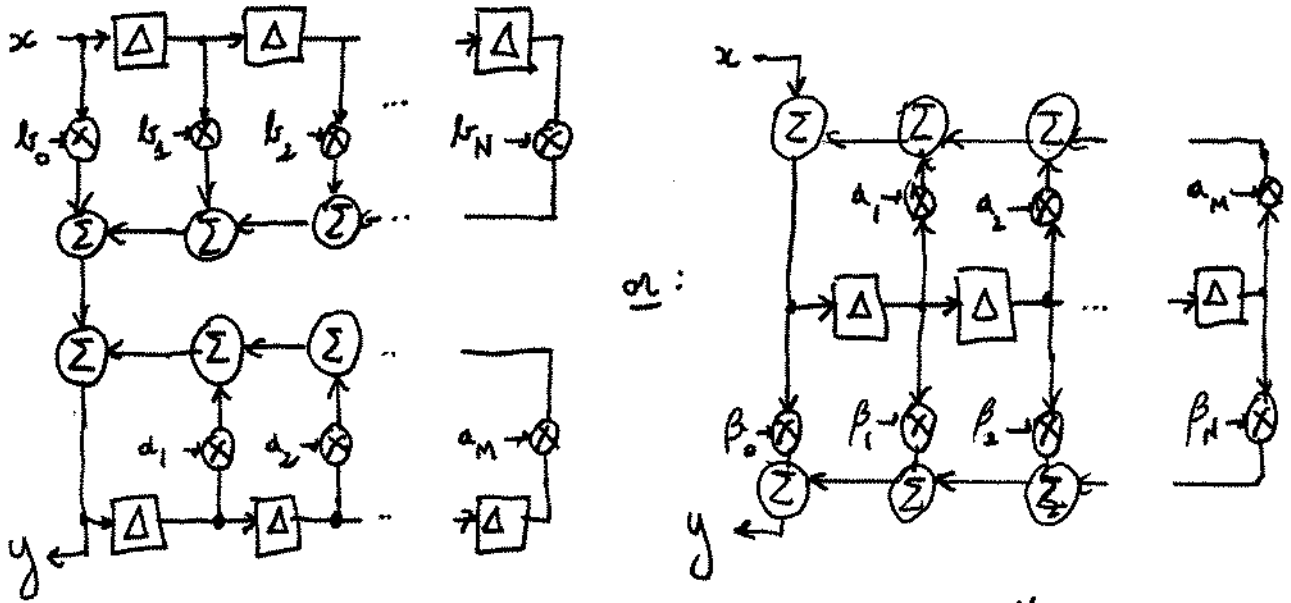
maps the frequency domains of both representations with a one-to-one correspondence (though distorted)



DIGITAL DISCRETE-TIME IMPLEMENTATION

• Infinite Impulse Response (IIR) : $H(z) = \frac{\sum_{j=0}^N b_j z^{-j}}{1 - \sum_{i=1}^M a_i z^{-i}}$

$\rightarrow y^k = \sum_{i=1}^M a_i y^{k-i} + \sum_{j=0}^N b_j x^{k-j}$



• Finite Impulse Response (FIR) : $H(z) = \sum_{j=0}^N b_j z^{-j}$

$\rightarrow y^k = \sum_{j=0}^N b_j x^{k-j}$

